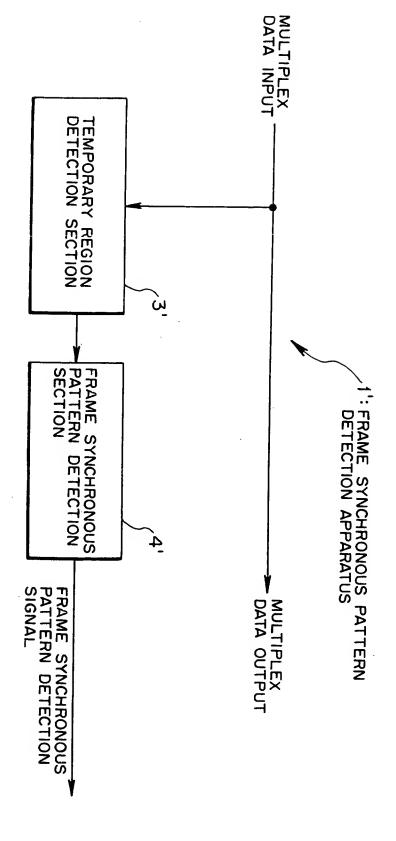




FIG.2



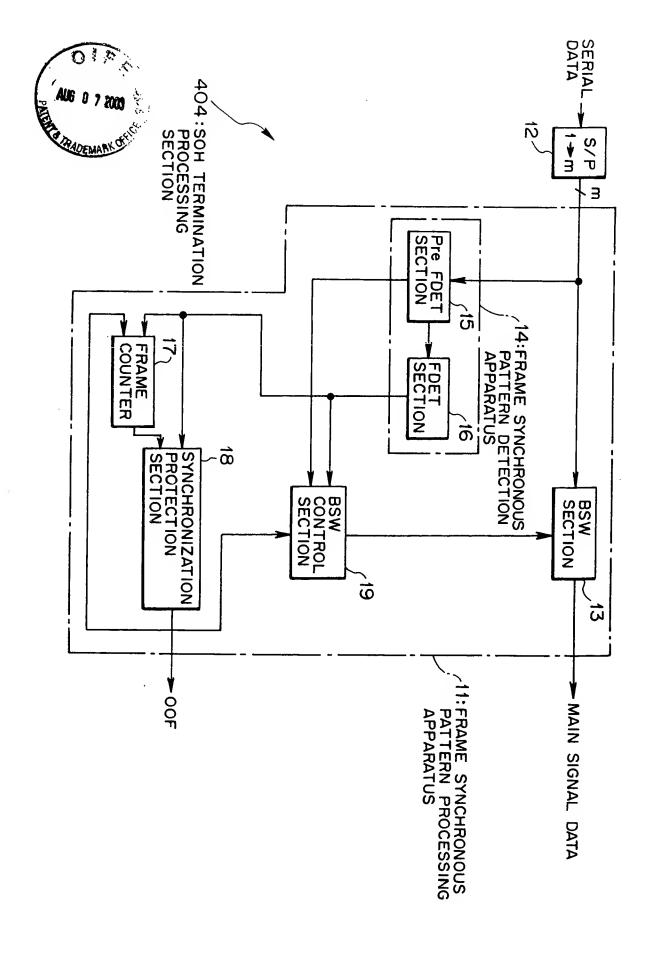
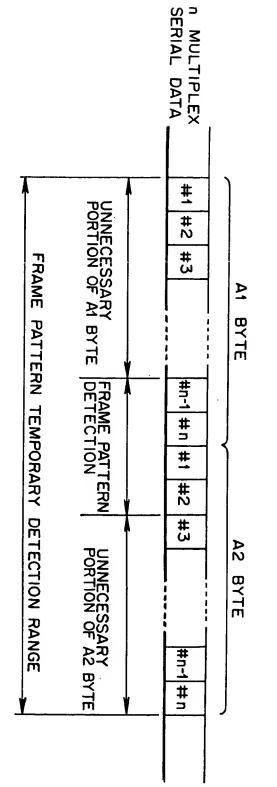


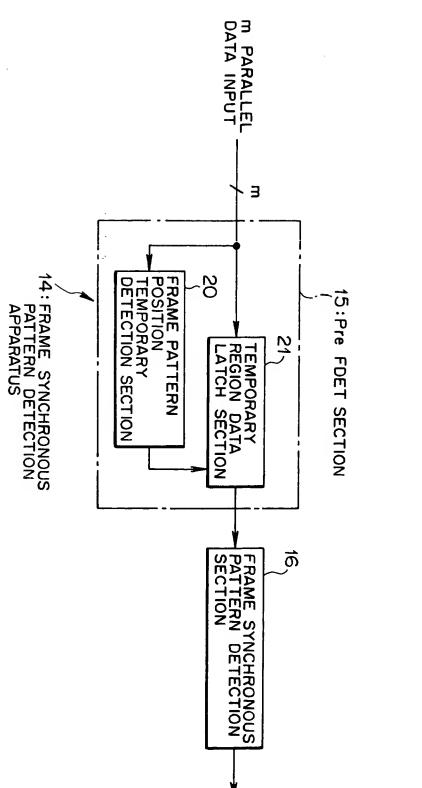
FIG.3



-IG.4







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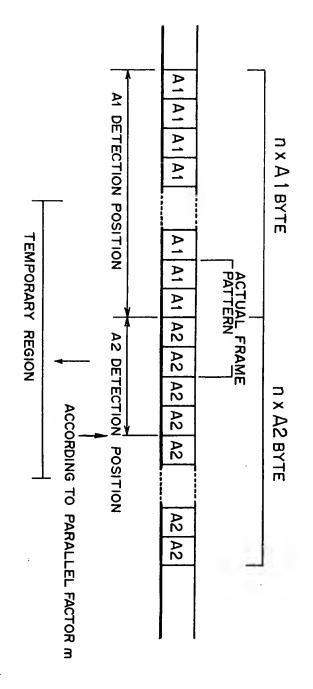
m PARALLEL DATA INPUT 24 A1 BYTE DETECTION SECTION 3 20: FRAME PATTERN POSITION TEMPORARY DETECTION SECTION SWITCHING CONTROL SECTION 22 15: Pre FDET SECTION A2 BYTE DETECTION SECTION 23 TEMPORARY
REGION DATA
LATCH SECTION LATCH ù SERIAL DATA OUTPUT

FIG.6



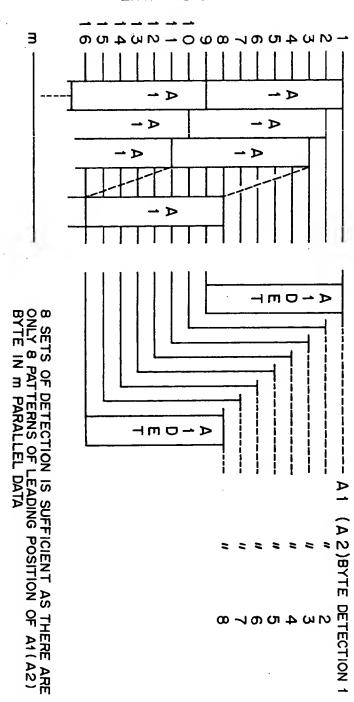
## F | G | 7

• SUPPOSE THE ACTUAL FRAME PATTERN BE 4 BYTES AT INCLUDING AL/A2 BOUNDARY

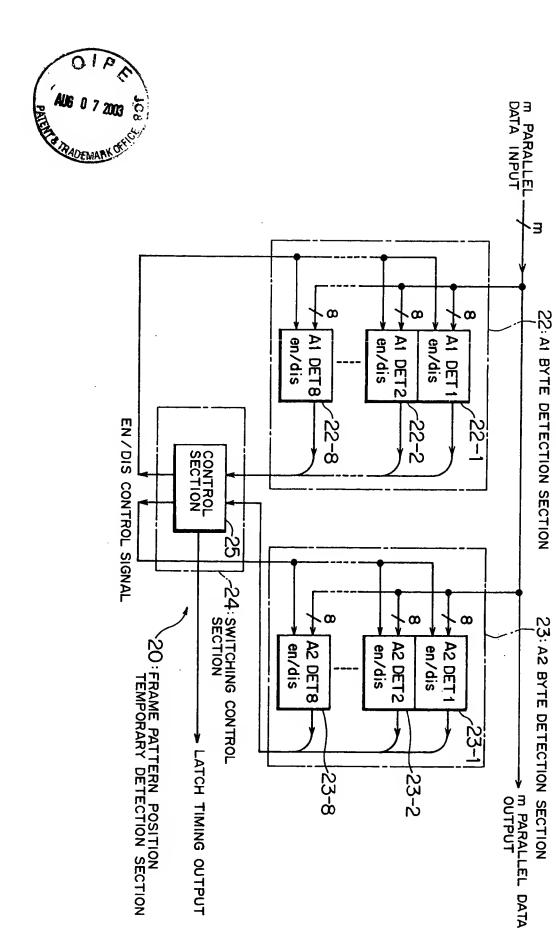




TURNI ATAO LIEL DARA M



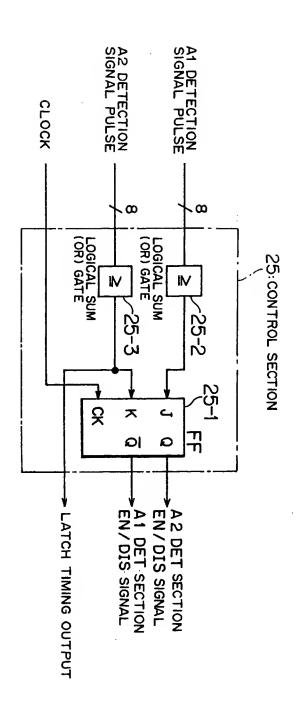
F1G. 8



F | G. 9

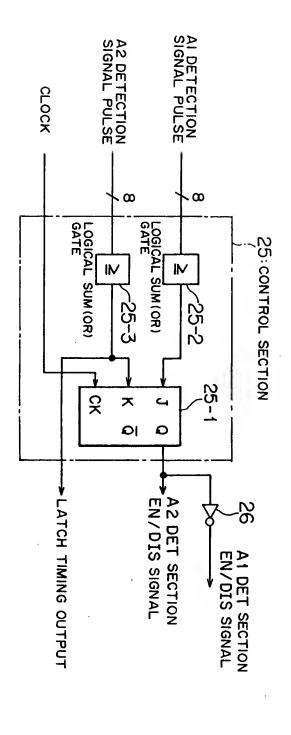


## - 16. 10

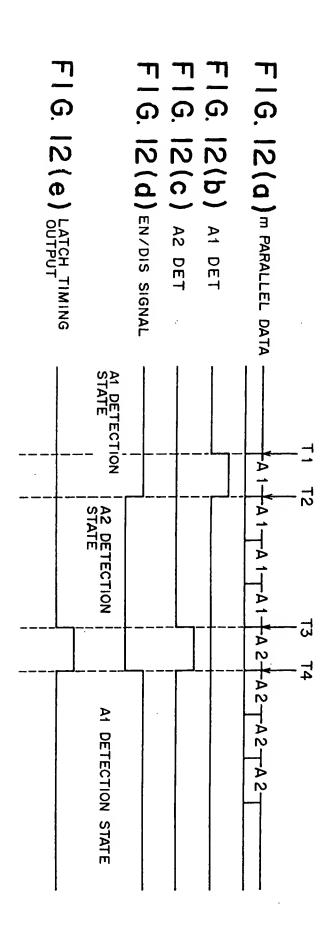














F | G. | 3 (a) CLOCK

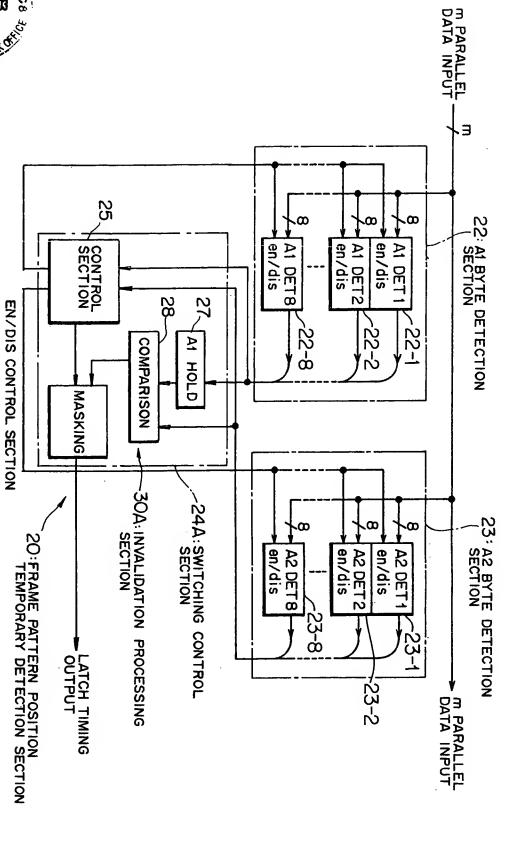
FIG. 13(b) SIGNAL PULSE FIG. 13(c) SIGNAL PULSE

FIG. 13(e) LATCH TIMING

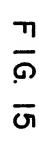
FIG. 13(d) A2 DET SECTION EN / DIS SIGNAL

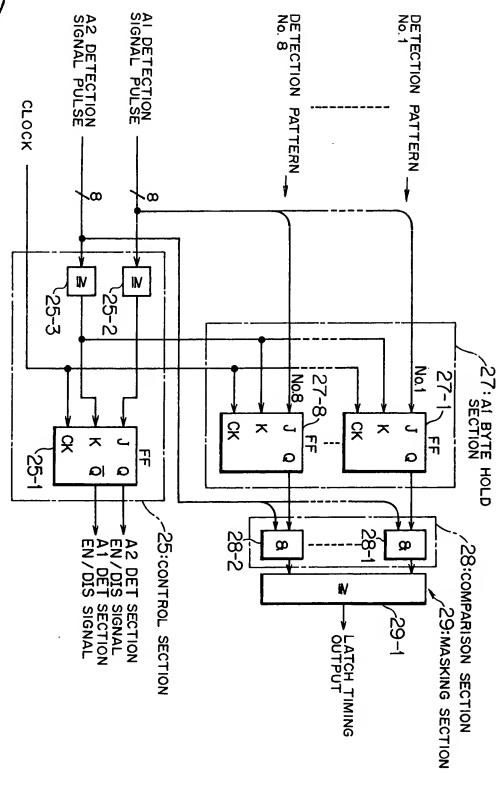


F I G. 14

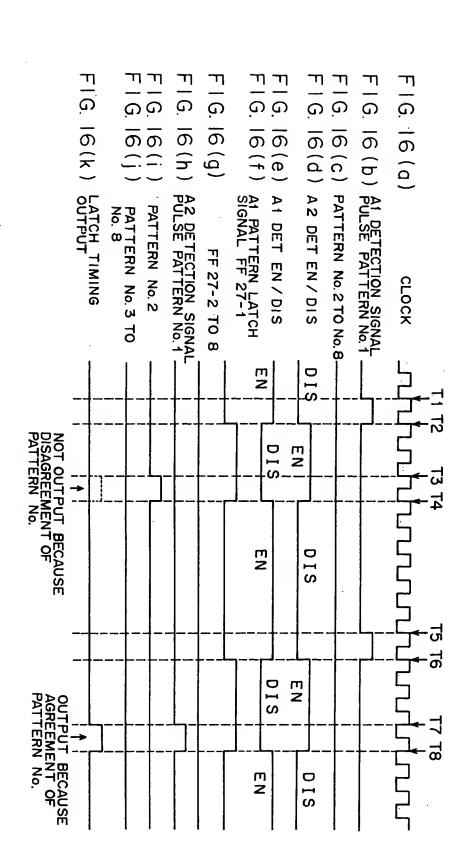




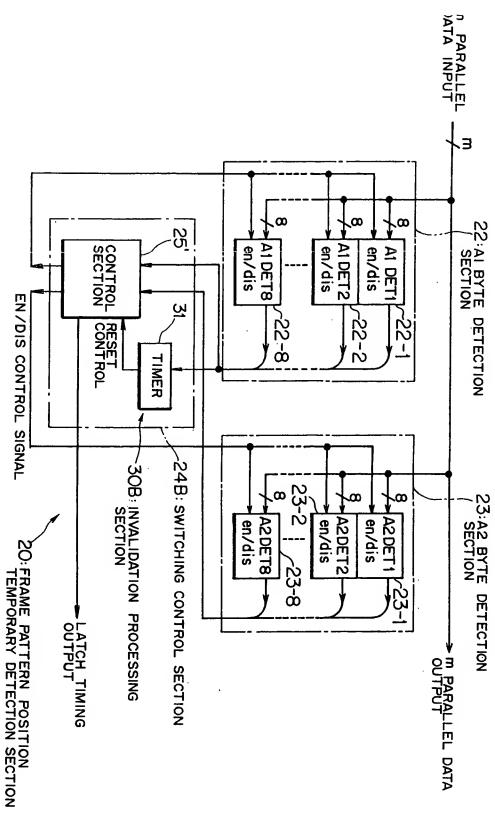








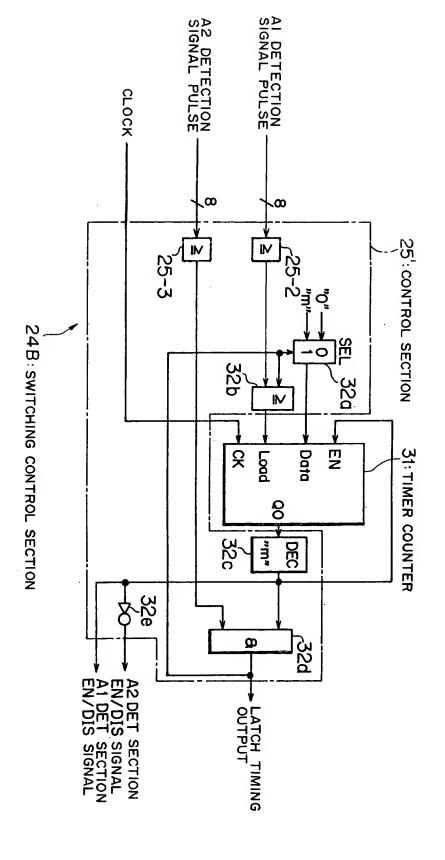




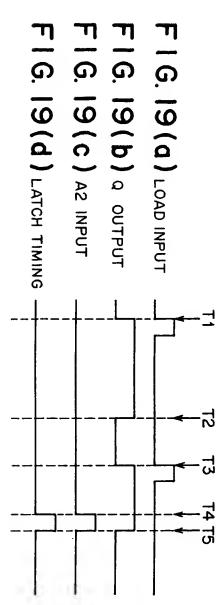
F | G. | 17



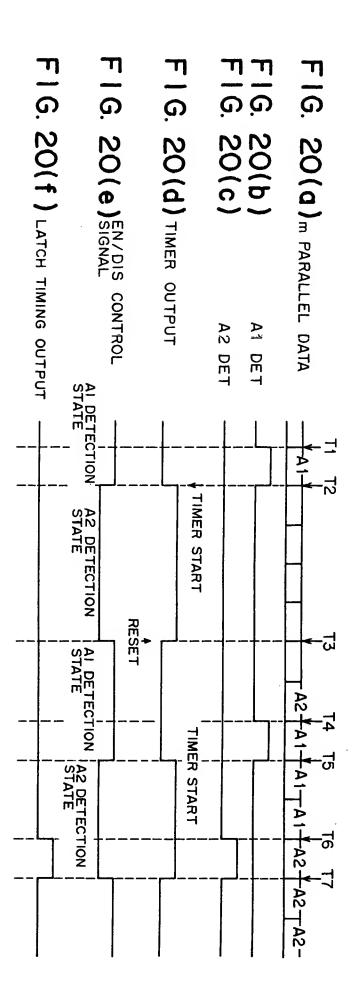
FIG. 18



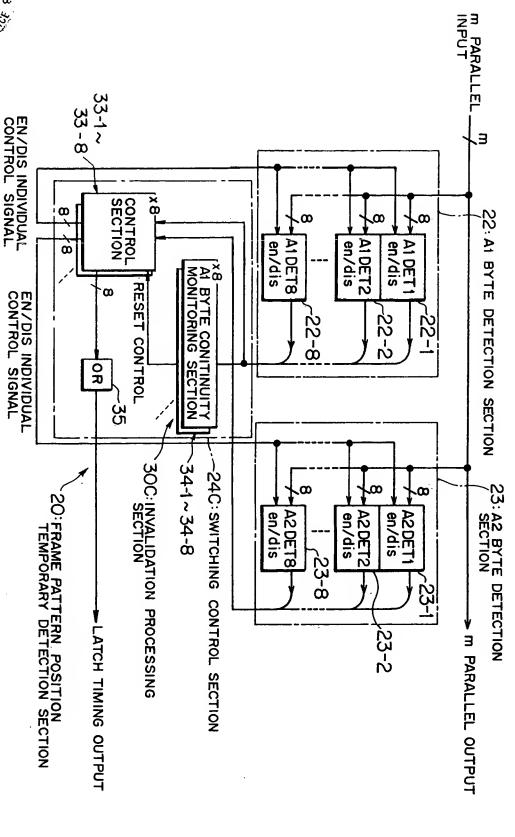












F | G. 2|





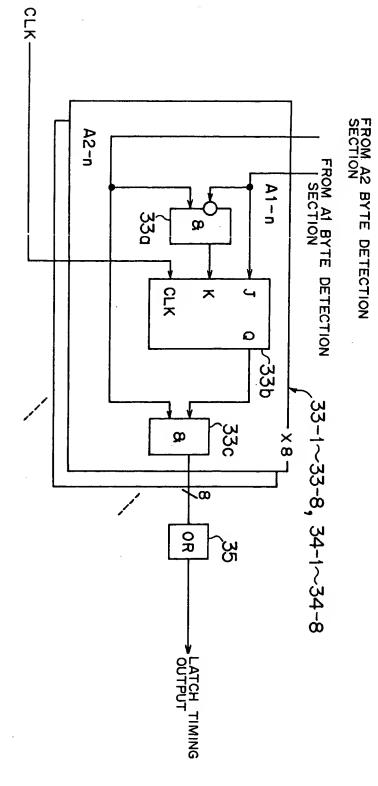




FIG. 23 (a) m PARALLEL DATA 1 G. 23(b) 1 G. 23(c) 1 G. 23(d)

A1 DET

EN/DIS CONTROL

A2 DET

FIG. 23(e) LATCH TIMING OUTPUT DETECTION CIRCUIT DISABLED EXCEPT INITIAL DETECTION FA 1 ZT 2 13 **4** -A 1-15 <u>5</u> FA1<del>-</del>FA2+A2→A2-8T RESET



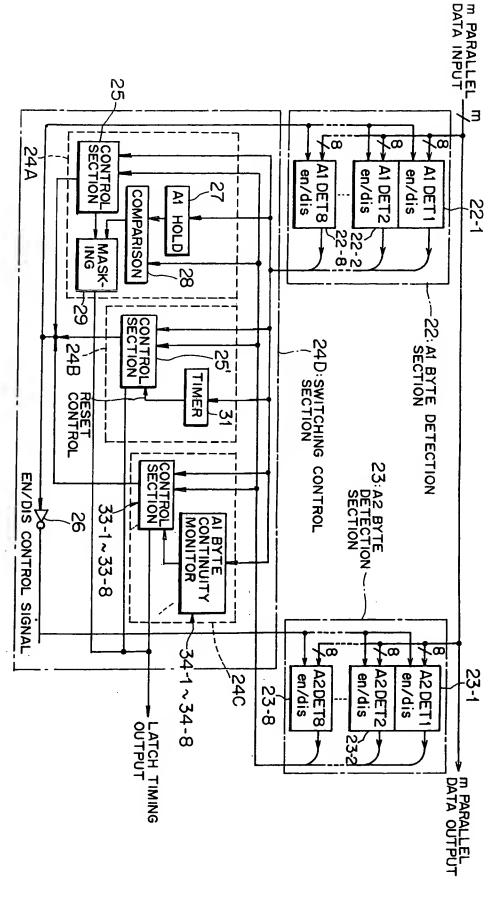
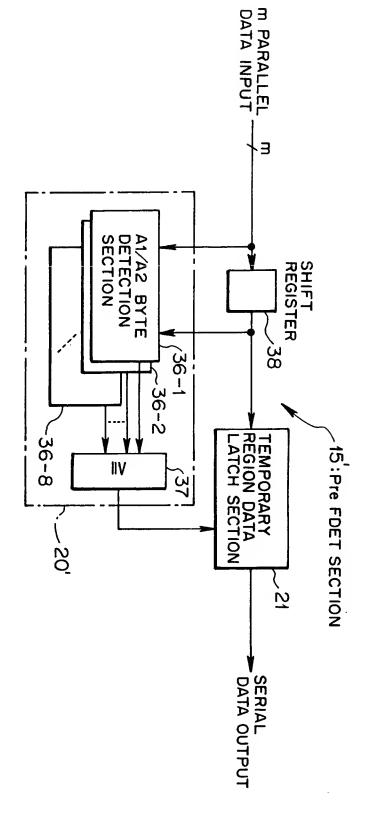


FIG. 24









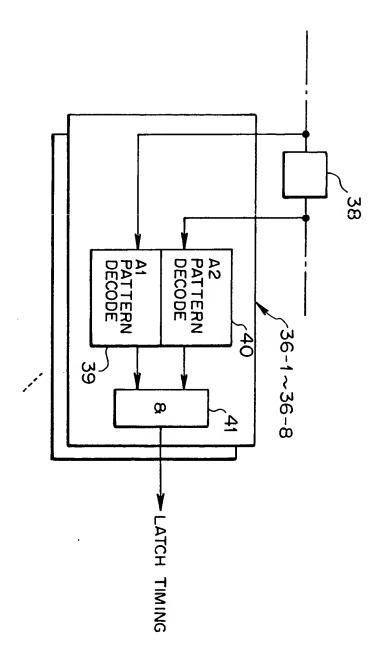
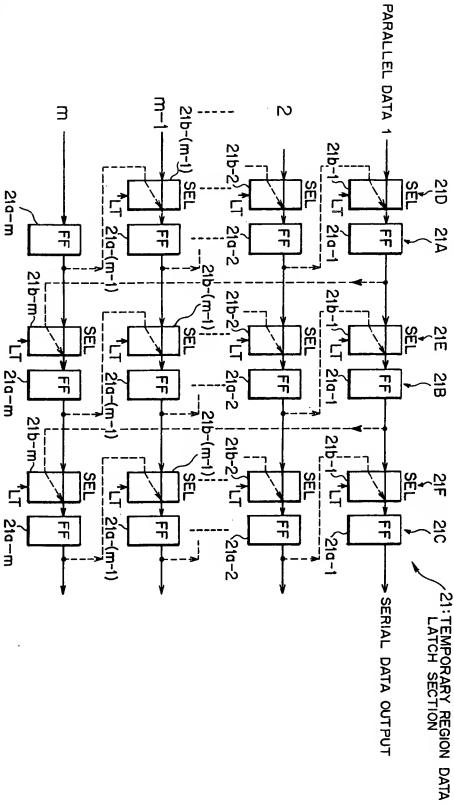


FIG.26



FIG. 27 (a) SERIAL DATA FIG. 27(b) 8 BITS 4 BITS 4 BITS A1 n x A1 BYTE ≥ D A Þ A1 A1 Þ A A2 A2 A2 Þ m PARALLELIZATION (1F m =16) N n x A2 BYTE D Ŋ A2 A2 D 2 Þ A2 A2 N

F I G. 28



LT: LATCH TIMING SIGNAL

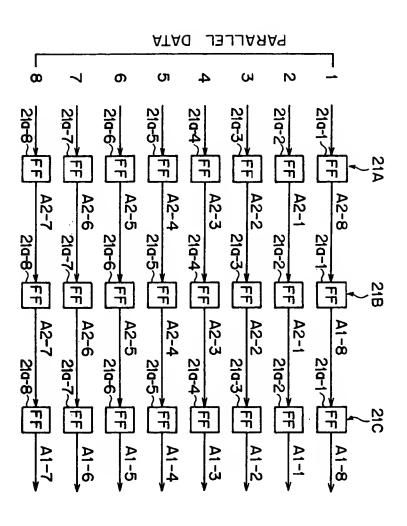
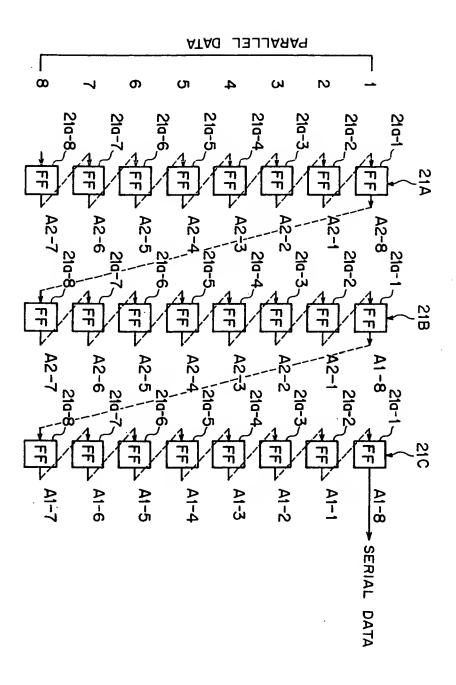


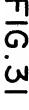
FIG. 29

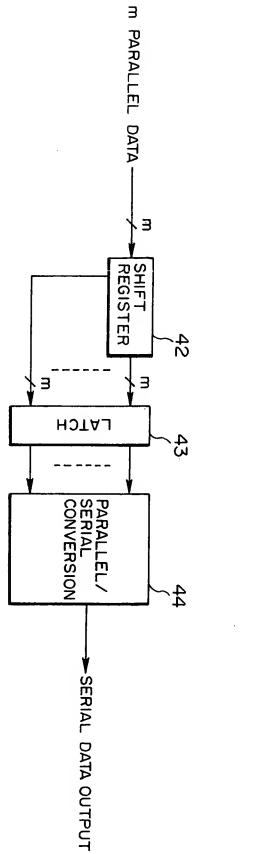




# F1G. 30

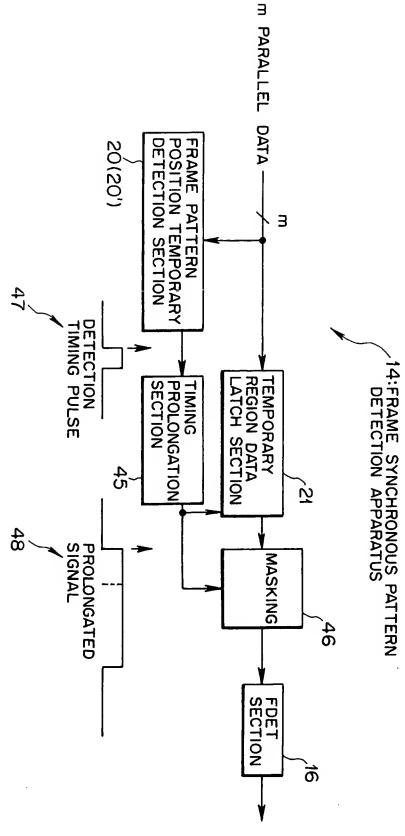






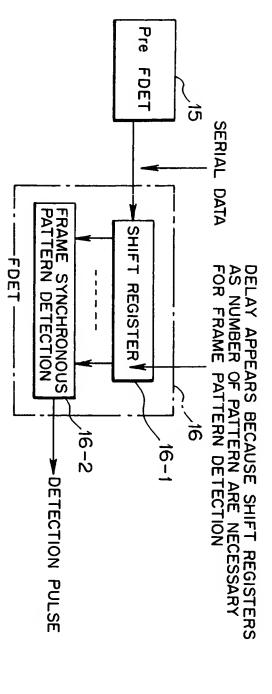


## FIG. 32





## FIG. 33



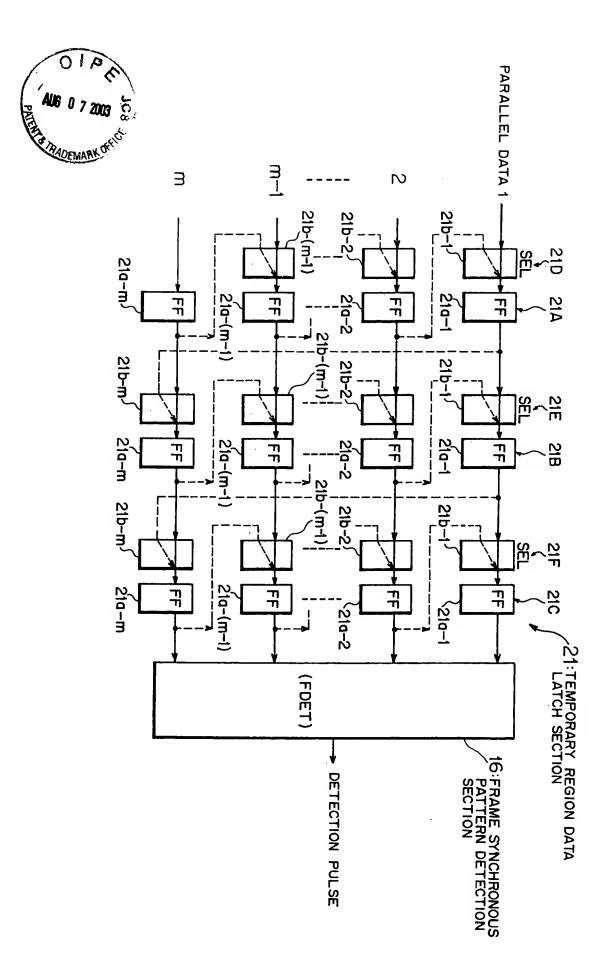
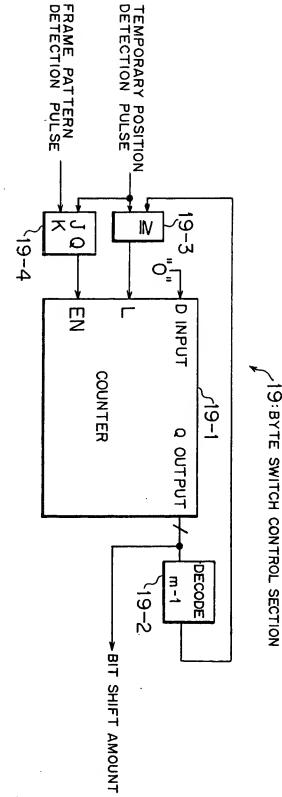


FIG. 34



## FIG. 35



k



### FIG. 36(a)

PRE FDET OUTPUT
TEMPORARY
POSITION DETECTION
PULSE

FIG. 36(b)

FDET OUTPUT
FRAME PATTERN
DETECTION PULSE

BIT SHIFT AMOUNT

### FIG. 37

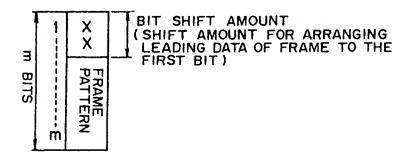




FIG. 38

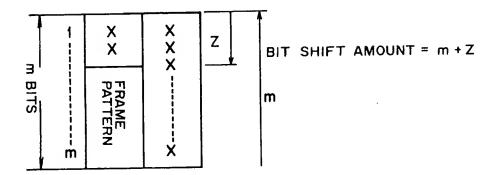




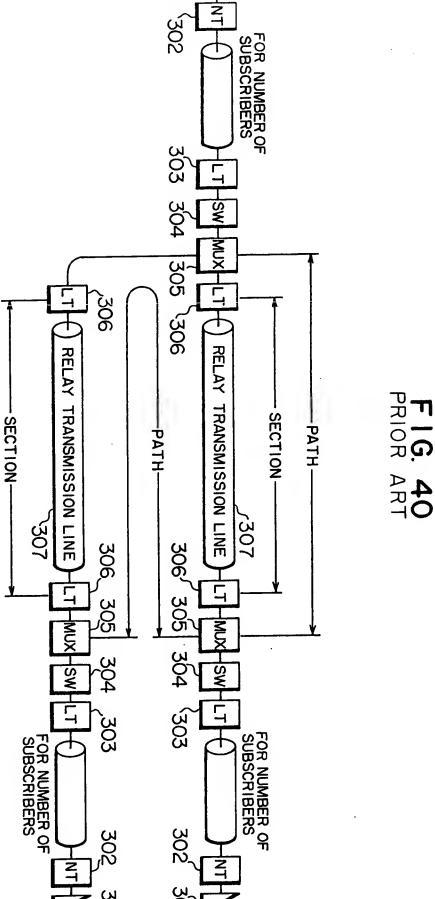
FIG. 39(d) COUNTER L (LOAD) FIG. 39(b) FRAME PATTERN FIG. 39(c) COUNTER EN (ENABLE) FIG. 39 (a ) TEMPORARY POSITION PULSE COUNTER OPERATION

FIG. 39(e) COUNTER L VALUE

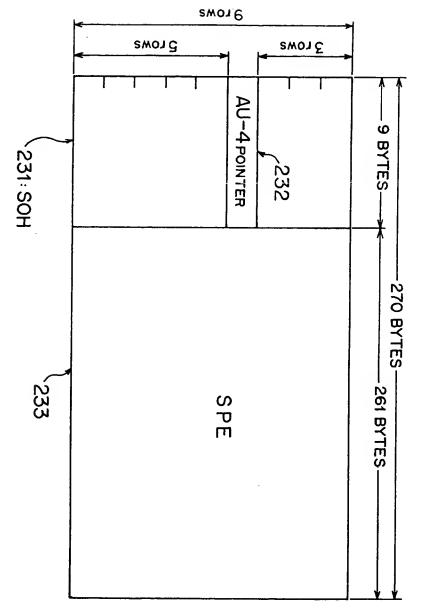
3

7









F | G. 4| PRIOR ART



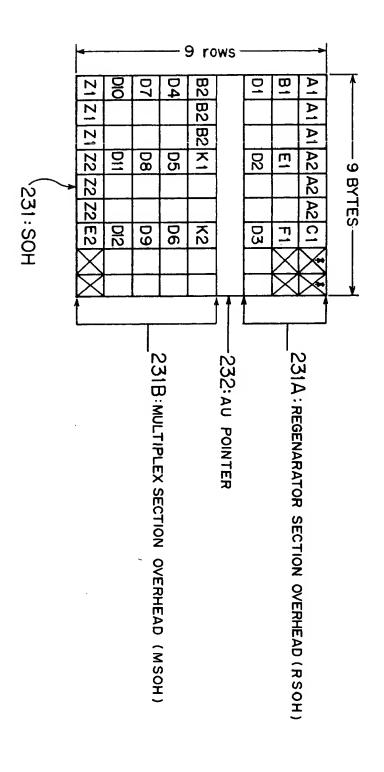


FIG. 42 PRIOR ART



SWOR 6-Dio. **D7** 2 82 8282828282828282828282 K1 <u>0</u> ≥  $\overline{\omega}$ ₽ A1 02 Ē 90 05 22 A2 C1 C1 C1 C1 231: S0H 36 BYTES D3 Ŧ **6**0 96 20 \_231A: RSOH 232: AU POINTER 231B: MSOH

FIG. 43 PRIOR ART

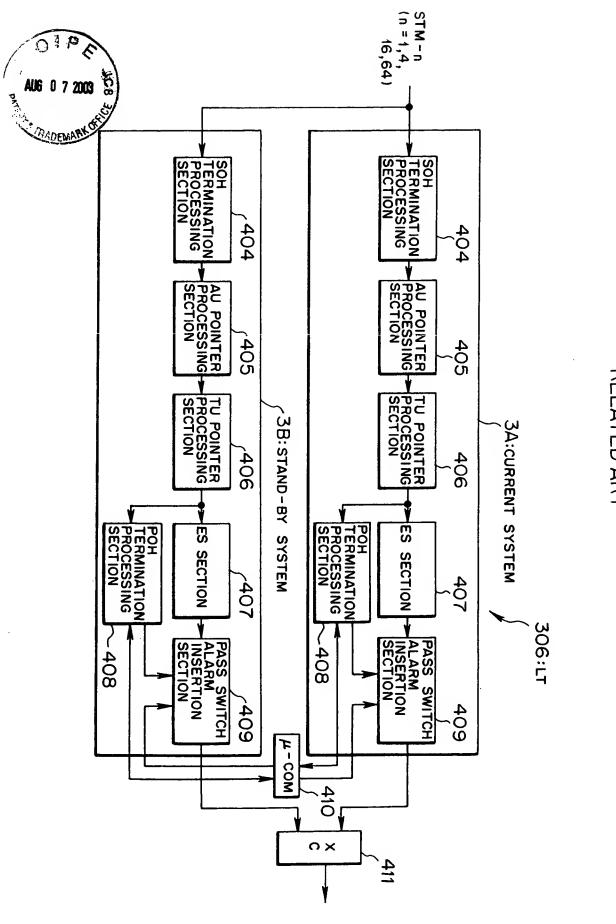
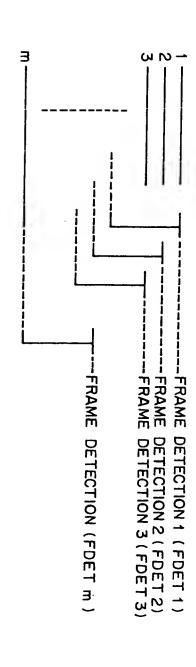


FIG. 44 RELATED ART



m PARALLEL DATA

FIG. 45 RELATED ART



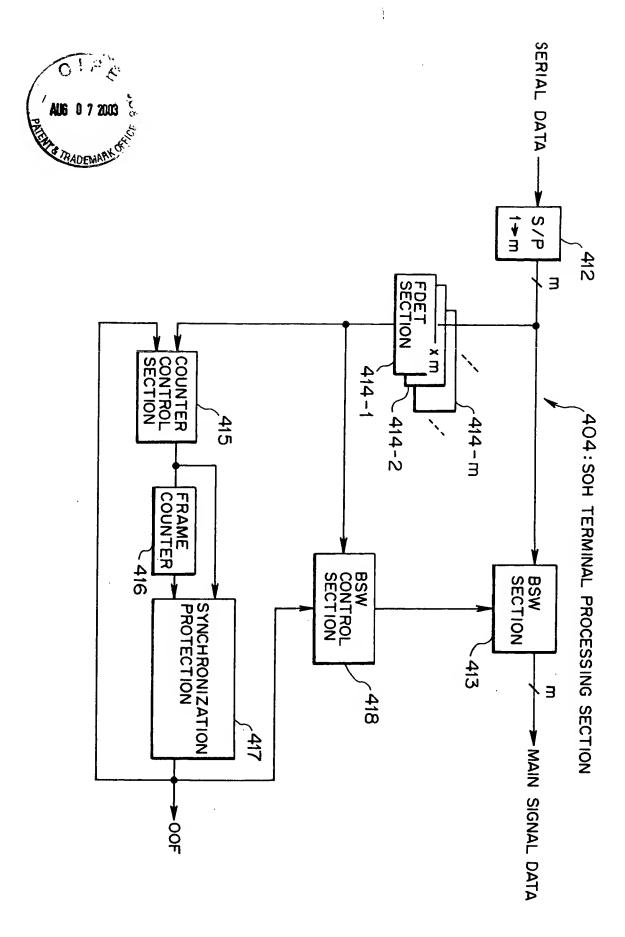


FIG.46
RELATED ART



#### FIG.47 RELATED ART



### FIG.48 RELATED ART

